股票代码 871699

PDFN3*3 Plastic-Encapsulate

型号: SLS65N06

60V N-Channel Enhancement Mode MOSFET

The SLS65N06DF uses advanced APM-SGT I technology to provide excellent RDS(ON), low gate charge and

operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

主要特性/Features

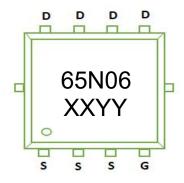
N-MOSFET

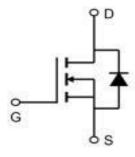
VDS = 60V ID =65A RDS(ON) < 10mΩ @ VGS=10V (Type: $7.5m\Omega$)

应用/Application

Battery Switch .
Load switch .
Power management.
Uninterruptible power supply

印字/MARKING 等效电路/Equivalent Circuit





股票代码 871699

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极限参数/ P-MOSFET Absolute Maximum Ratings(TA=25°Cunless otherwise noted)

Symbol	Parameter	Value	Unit
VDS	Drain source voltage	60	V
VGS	Gate source voltage	±20	V
Id@Ta=25°C	Continuous drain current	60	А
Id@Ta=70°C	Continuous drain current	31	А
IDM	Pulsed drain current	60	А
Pd@Ta=25°C	Power dissipation	60	W
EAS	Single pulsed avalanche energy	30	mJ
TSTG	Storage Temperature Range	-55 to 150	°C
Тј	Operation and storage temperature	-55 to 150	°C
RθJC	Thermal resistance, junction-case	2.1	°C/W
RθJA	Thermal resistance, junction-ambient5)	85	°C/W

股票代码 871699

PDFN3*3 Plastic-Encapsulate

电性能参数/ P-MOSFET Electrical Characteristics (TA=25°Cunless otherwise noted)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
BVDSS	Drain-source breakdown voltage	Vgs=0 V, Ip=250 μA	60	68		V
VGS(th)	Gate threshold voltage	V _{DS} =V _{GS} , I _D =250 μA	1.2	1.5	2.5	V
RDS(ON)	Drain-source on-state resistance	Vgs=10 V, ID=20 A		7.5	10	mΩ
RDS(ON)	Drain-source on-state resistance	Vgs=4.5 V, Ip=10 A		10	13	mΩ
IGSS	Gate-source leakage current	V _{GS} =±20 V			±100	nA
IDSS	Drain-source leakage current	V _{DS} =60 V, V _{GS} =0 V			1	μA
Ciss	Input capacitance			1182.1		pF
Coss	Output capacitance	Vgs=0 V, Vps=50 V, f=100 kHz		199.5		pF
Crss	Reverse transfer capacitance			4.1		pF
td(on)	Turn-on delay time	V _G s=10 V, V _D s=50 V, R _G =2 Ω, I _D =10 A		17.9		ns
tr	Rise time			4.0		ns
td(off)	Turn-off delay time			34.9		ns
tf	Fall time			5.5		ns
Qg	Total gate charge			18.4		nC
Qgs	Gate-source charge	lɒ=10 A, Vɒs=50 V,		3.3		nC
Qgd	Gate-drain charge	V _{GS} =10 V		3.1		nC
Vplateau	Gate plateau voltage			2.8		V
ls	Diode forward current	V00 V(I			60	Α
ISP	Pulsed source current	VGS <vth< td=""><td></td><td></td><td>180</td><td></td></vth<>			180	
VSD	Diode forward voltage	Is=20 A, V _G s=0 V			1.3	V
trr	Reverse recovery time			41.8		ns
Qrr	Reverse recovery charge	Is=10 A, di/dt=100 A/µs		36.1		nC
Irrm	Peak reverse recovery current			1.4		Α

Note

- 1、Calculated continuous current based on maximum allowable junction temperature.
- 2、Repetitive rating; pulse width limited by max. junction temperature.
- 3、Pd is based on max. junction temperature, using junction-case thermal resistance.
- 5_{\circ} The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a =25 $^{\circ}$ C.

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典型特性/ Typical Characteristics

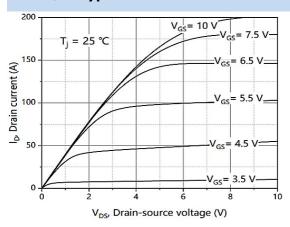


Figure 1. Typ. output characteristics

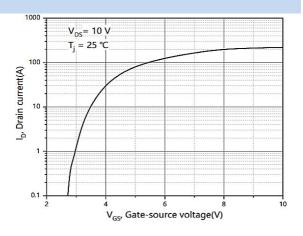
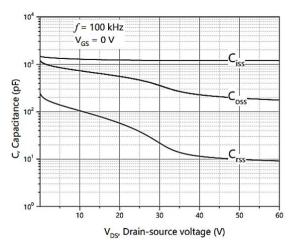


Figure 2. Typ. transfer characteristics



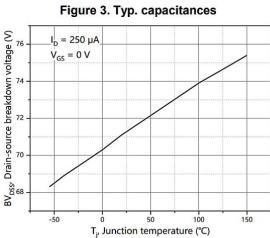


Figure 5. Drain-source breakdown voltage

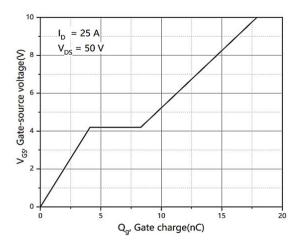


Figure 4. Typ. gate charge

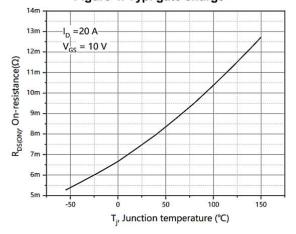


Figure 6. Drain-source on-state resistance

股票代码 871699

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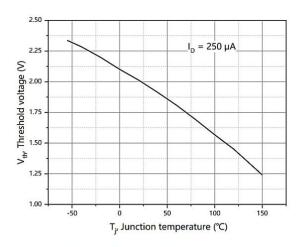


Figure 7. Threshold voltage

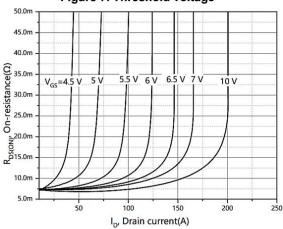


Figure 9. Drain-source on-state resistance

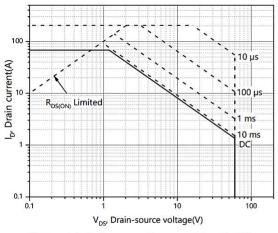


Figure 11. Safe operation area T_C=25 °C

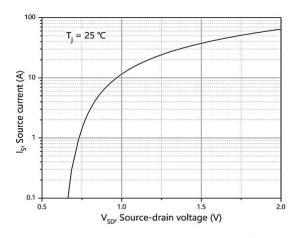


Figure 8. Forward characteristic of body diode

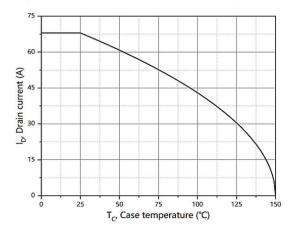


Figure 10. Drain current

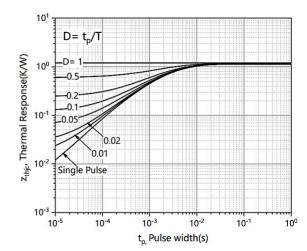
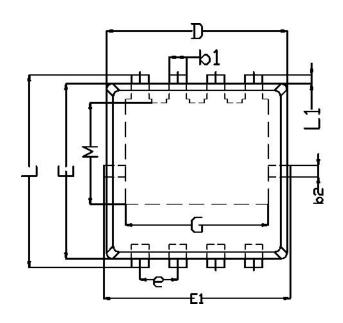


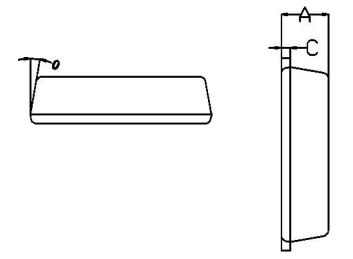
Figure 12. Max. transient thermal impedance

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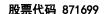
成品外观尺寸/PDFN3*3 Package Information



Syabol	Din in mi			
	Min	Nom	Max	
A	0.75	0.80	0.85	
LI	0.10	0.15	0.20	
bl	0.25	0.30	0.35	
b2	0.15	0.20	0.25	
C	0.10	0.15	0.20	
D	3.050	3.100	3.150	
e	0.650OSO			
E	2.950	3.000	3.050	
El	3.150	3.200	3.250	
L	3.250	3.300	3.350	
M	L685	1.735	1.785	
G	2.400	2.450	2.500	
0	0"	5,	w	



深圳市三咬	E3 0	Date:	TITLE: PDFN3X3-8L Package outline	
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